

			35 GND										
Pinko			Samosa		Aux parallel I/O			LCD and touchscreen			USB OTG		
J13 (top)			J6 (top)		J7 (bottom)			J8 (bottom)			J3 (top)		
As TCL design on Pikey 2, plus USB and power					Pattern of VDD/GND as samosa				35-way 0.5mm FFC, top contact				
30-way 0.5mm FFC, top contact			35-way 0.5mm FFC, top contact		35-way 0.5mm FFC, top contact			eg Farnell 309-1533			USB mini B conn		
eg Farnell 307-8346			eg Farnell 309-1533		Not fitted			on bottom of board					
1 GND			1 GND		1 GND		1 GND		1 GND		1 VBUS		
2 PINKO_UNUSP	GPIO1		2 SAMOSA_CLE	Xilinx	2 BP_D3	Xilinx	2 LCD_PCLK_OUT	Xilinx	2 D-				
3 NRES_BUTTON			3 SAMOSA_ALE	Xilinx	3 BP_D11	Xilinx	3 LCD_LCLK_A0	GPIO75	3 D+				
4 PINKO_TXD	Xilinx		4 SAMOSA_NWE	Xilinx	4 BP_D4	Xilinx	4 LCD_FCLK_RD	GPIO74	4 ID				
5 PINKO_RXD	Xilinx		5 SAMOSA_NWP	Xilinx	5 BP_D12	Xilinx	5 LCD_BIAS	GPIO77	5 GND				
6 PINKO_CTS	Xilinx		6 SAMOSA_NRE	Xilinx	6 BP_D5	Xilinx	6 LCD_CS	GPIO19					
7 GND			7 VDD_SAMOSA		7 VDD_SAMOSA		7 LCD_VSYNC	GPIO14					
8 COM2/FFTXD	GPIO16		8 SAMOSA_NCE	Xilinx	8 BP_D13	Xilinx	8 VDD_PANEL						
9 COM2/FFRXD	GPIO96		9 SAMOSA_RNB	Xilinx	9 BP_D6	Xilinx	9 LCD_D0	GPIO58					
10 COM3/STD_TXD_232	GPIO47		10 SAMOSA_WP	Xilinx	10 BP_D14	Xilinx	10 LCD_D1	GPIO59					
11 COM3/STD_RXD_232	GPIO46		11 SAMOSA_ABSENT	Xilinx	11 BP_D7	Xilinx	11 LCD_D2	GPIO60					
12 GND			12 SAMOSA_INTERRUPT	Xilinx	12 BP_D15	Xilinx	12 LCD_D3	GPIO61					
13 AUDIO_OUT_L			13 RESET (output)		13 BP_A10		13 LCD_D4	GPIO62					
14 AUDIO_OUT_R			14 GND		14 GND		14 LCD_D5	GPIO63			Backup battery		
15 GND			15 SAMOSA_D0	Xilinx	15 BP_A9	Xilinx	15 LCD_D6	GPIO64			J4 (bottom)		
16 no connection - was VDD_BACKLIGHT			16 SAMOSA_D1	Xilinx	16 BP_A8	Xilinx	16 LCD_D7	GPIO65					
17 GND			17 SAMOSA_D2	Xilinx	17 BP_A7	Xilinx	17 GND				2-way Molex 1.25mm		
18 VDD_BACKUP			18 SAMOSA_D3	Xilinx	18 BP_A6	Xilinx	18 LCD_D8	GPIO66					
19 AUDIO_IN_L	should be r		19 VDD_SAMOSA		19 VDD_SAMOSA		19 LCD_D9	GPIO67			1 VDD_BACKUP		
20 GND			20 SAMOSA_D4	Xilinx	20 BP_A5	Xilinx	20 LCD_D10	GPIO68			2 GND		
21 GND			21 SAMOSA_D5	Xilinx	21 BP_A4	Xilinx	21 LCD_D11	GPIO69					
22 USB_OTG_UDC+/USB_HOST_UDC_2+			22 SAMOSA_D6	Xilinx	22 BP_A3	Xilinx	22 LCD_D12	GPIO70					
23 USB_OTG_UDC-/USB_HOST_UDC_2-			23 SAMOSA_D7	Xilinx	23 BP_A2	Xilinx	23 LCD_D13	GPIO71					
24 USB_OTG_VBUS/USB_HOST_PWR_2			24 GND		24 GND		24 LCD_D14	GPIO72					
25 USB_OTG_ID			25 SAMOSA_D8	Xilinx	25 BP_A1	Xilinx	25 LCD_D15	GPIO73					
26 GND			26 SAMOSA_D9	Xilinx	26 BP_A0	Xilinx	26 LCD_D16	GPIO86					
27 GND			27 SAMOSA_D10	Xilinx	27 BP_D0	Xilinx	27 LCD_D17	GPIO87					
28 VDD_RAW			28 SAMOSA_D11	Xilinx	28 BP_D1	Xilinx	28 RUN_BACKLIGHT	GPIO99					
29 VDD_RAW			29 VDD_SAMOSA		29 VDD_SAMOSA		29 SCL	GPIO117					
30 VDD_RAW			30 SAMOSA_D12	Xilinx	30 BP_D8	Xilinx	30 SDA	GPIO118					
			31 SAMOSA_D13	Xilinx	31 BP_D2	Xilinx	31 GND						
			32 SAMOSA_D14	Xilinx	32 BP_D9	Xilinx	32 TSPX						
			33 SAMOSA_D15	Xilinx	33 BP_D10	Xilinx	33 TSMX						
			34 GND		34 GND		34 TSPY						
			35 VDD_SAMOSA		35 VDD_SAMOSA		35 TSMY						